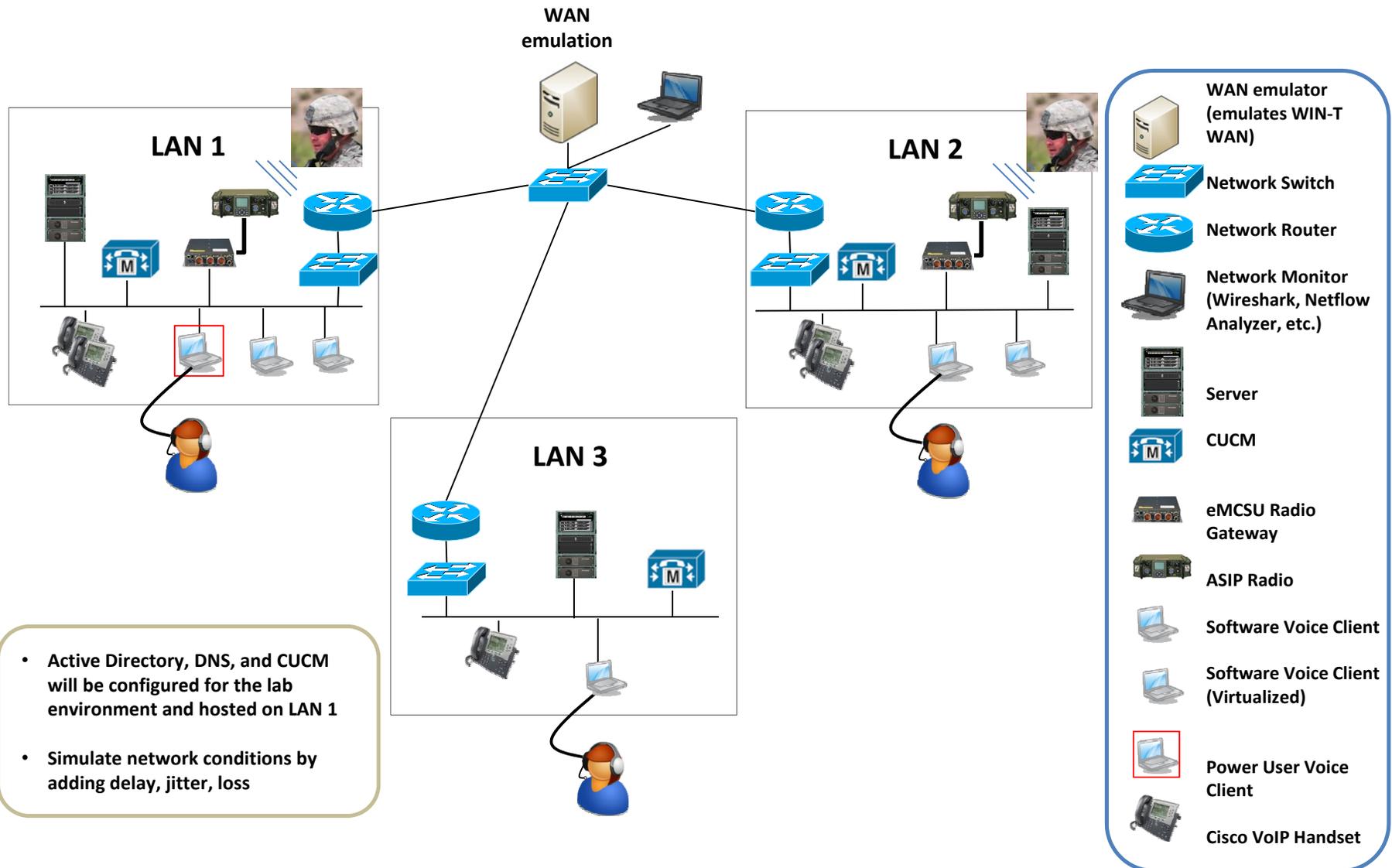


## STAGE II TEST ARCHITECTURE



## STAGE II TEST PROCESS FLOW

